DDR3 DIMM 2400 Interposer

For use with Keysight Logic Analyzers

FuturePlus Systems

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Power Tools for Bus Analysis

- **Improved DDR3 2400MT/s bus analysis with shortest Interposer design available**
- **System cost reduction. Direct connection to Keysight U4154A logic analyzers eliminates need for 4 U4201A adapter cables.**
- **Includes automatic logic analyzer configuration software and protocol-decode software.**
- **Interposer design does not consume a slot**
- **Provides Clock Qualification using CKE0:1 and Reset**

FS2361 DDR3 DIMM 2400 Interposer

Easy, Reliable DDR3 2400 Analysis

The FuturePlus® FS2361 DDR3 DIMM 2400 Interposer provides a mechanical, electrical and software interface between an Keysight logic analyzer and all signals on the DDR3 bus. The FS2361 is used to design and debug computer motherboards and DIMM's operating up to DDR3 2400 MT/s rates.

Key Features

- Quick and easy connection between the DDR3 DIMM SDRAM memory bus connector and Keysight logic analyzers
- Complete and accurate 2400 MT/s state and timing analysis up to 12.5 GHz
- Compatible with all 240-pin DDR3 SDRAM DIMM's up to 2400 MT/s.
- All signals are probed passively.
- Does not require U4201A cables, connections to the analyzer are built in
- RDIMM, UDIMM and LRDIMM are supported.
- Burst sizes of 4, or 8 are supported.
- Quick and easy setup using Keysight Eye Scan with 5 ps resolution

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Helping you Design Tomorrow's Computers, Today

FuturePlus Systems is the technology leader in protocol analysis tools for the computer design industry. Our Interposers and software help you monitor and verify complex activities on your advanced technology computer bus design. FuturePlus systems offerings include bus-analysis solutions for most popular computer buses. Visit www.futureplus.com for more information.

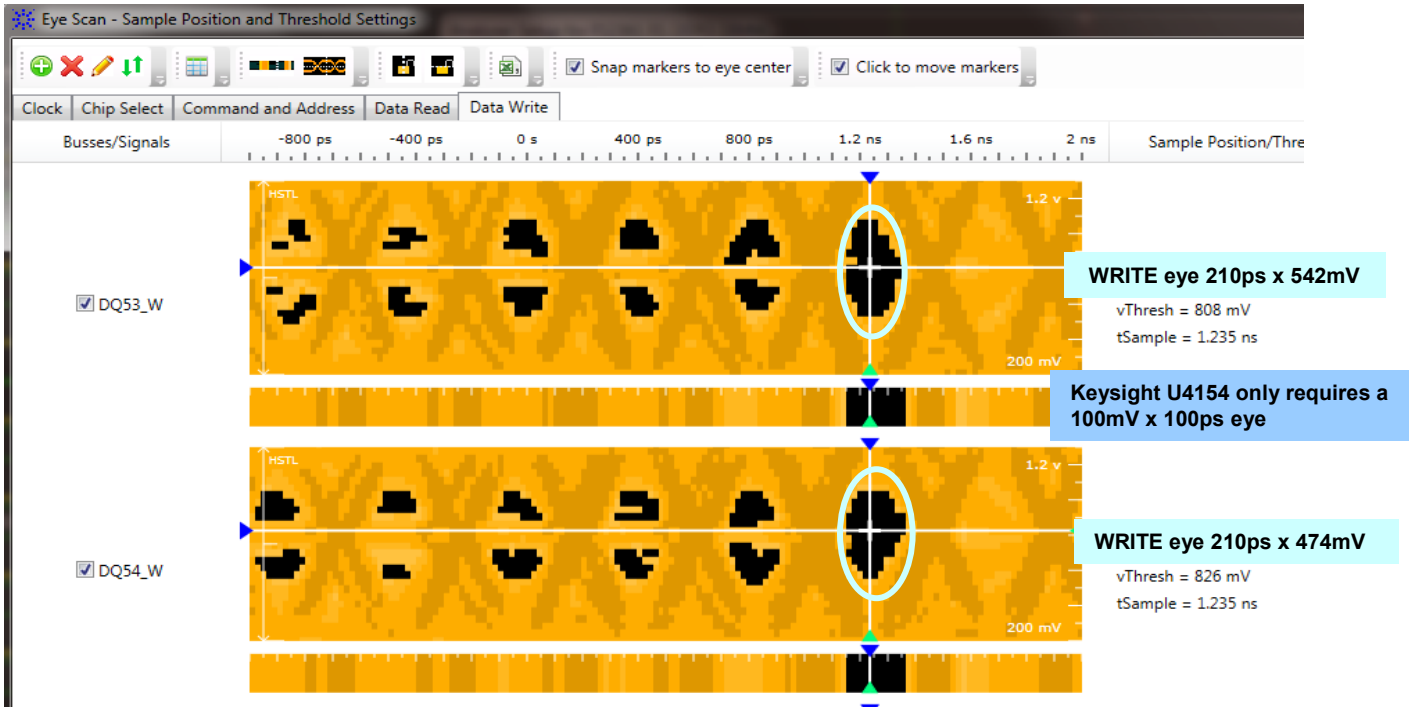
KEYSIGHT TECHNOLOGIES
Strategic Solutions Partner

Since 1991

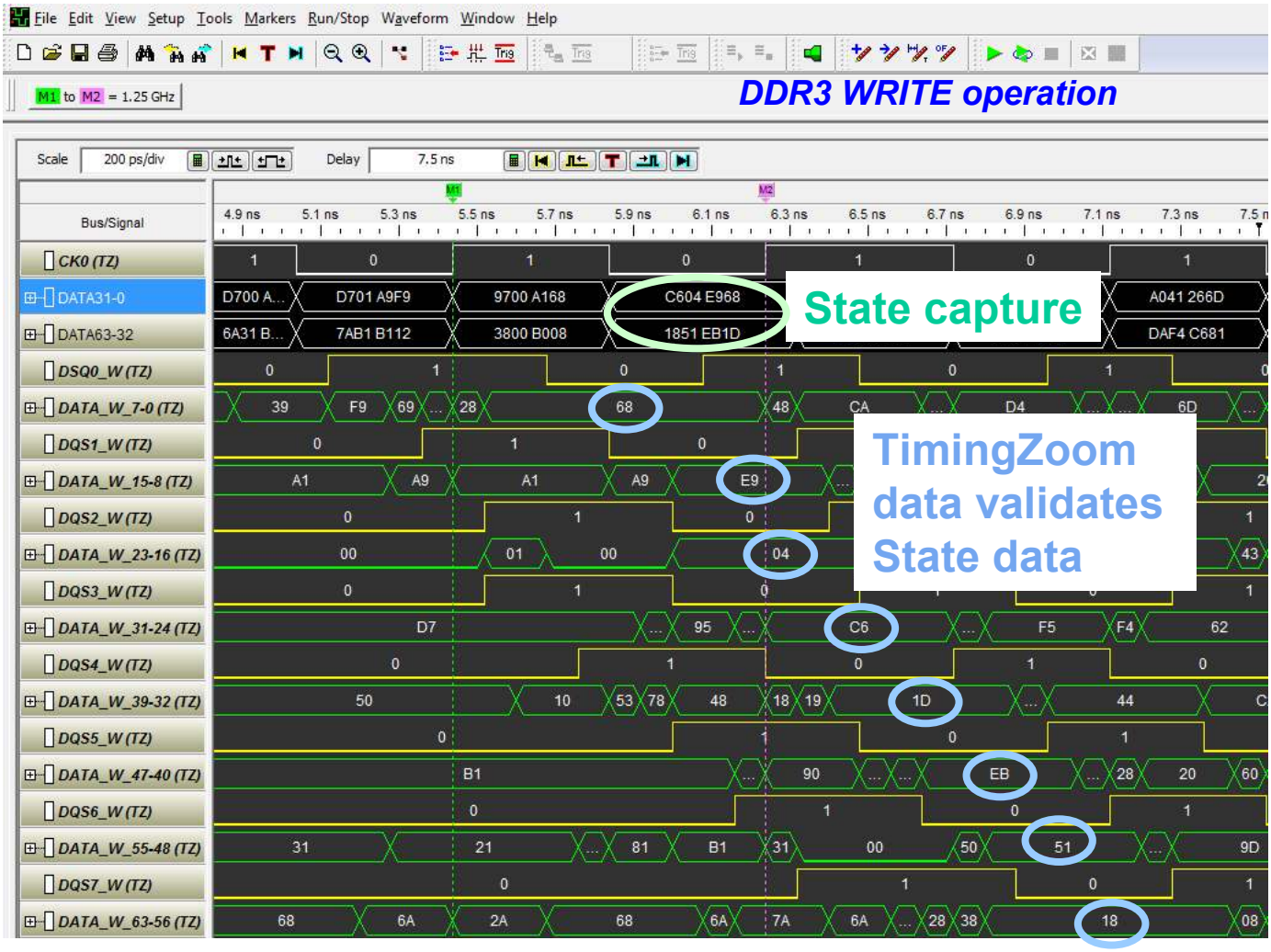
Demonstrated 2400 MT/s operation

DDR3 2400 MT/s Data READ Eyes from Eye Scan display on the FS2361

DDR3 2400 MT/s Data WRITE Eyes from Eye Scan display on the FS2361

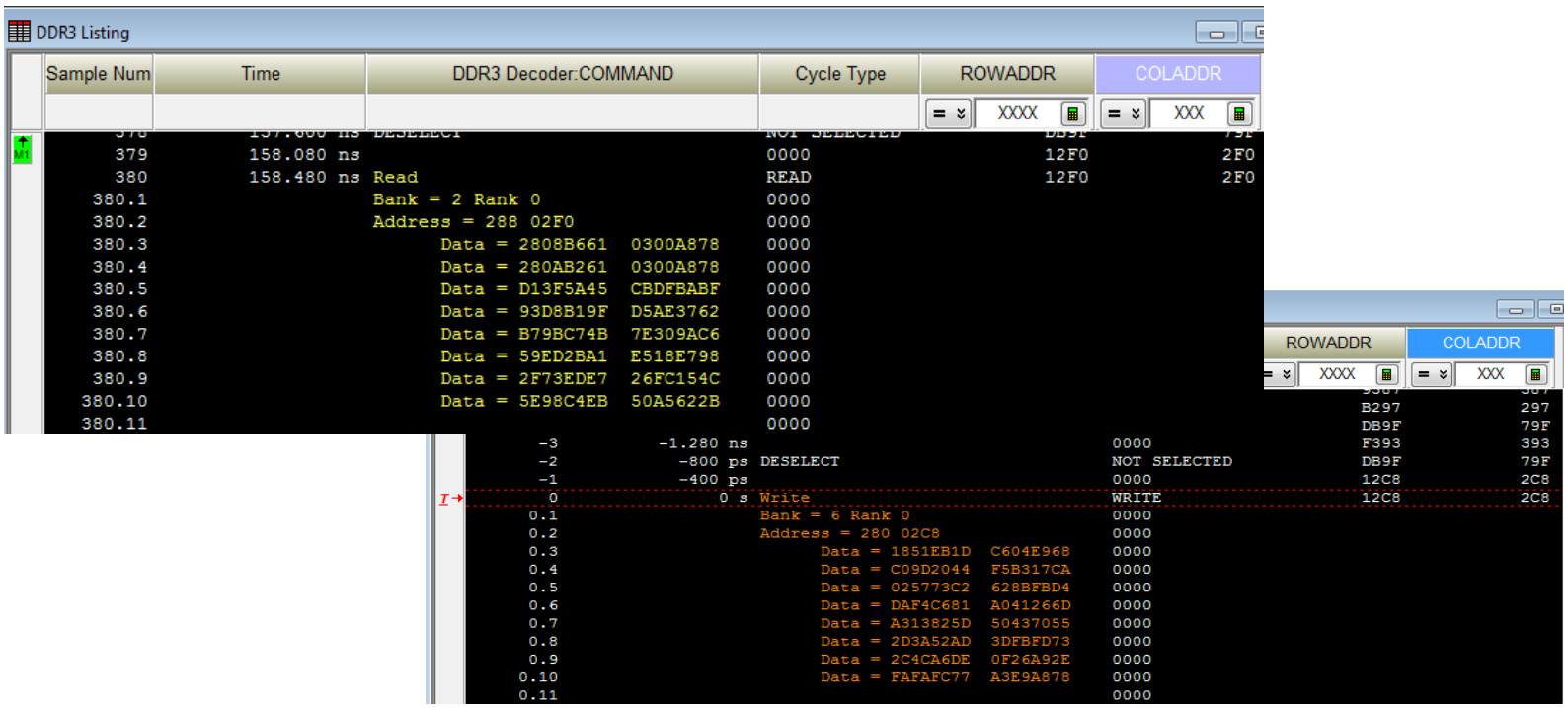


2400 MT/s State Capture



The FS2361 protocol-decode software translates acquired signals into easily understood bus transactions. This decode software executes in the logic analyzer to decode the DDR bus signals and present a display that lists the transaction type, address, data and command conditions.

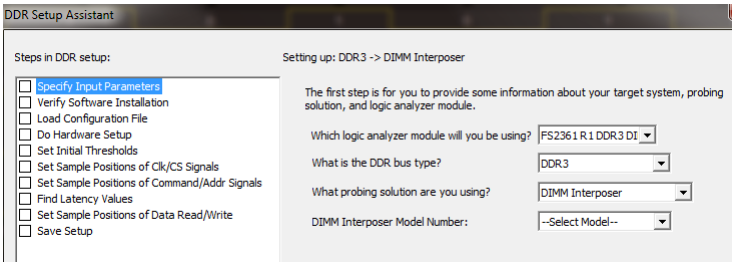
User-selectable post-processing allows the acquired data to be displayed as different types of transactions in different colors.

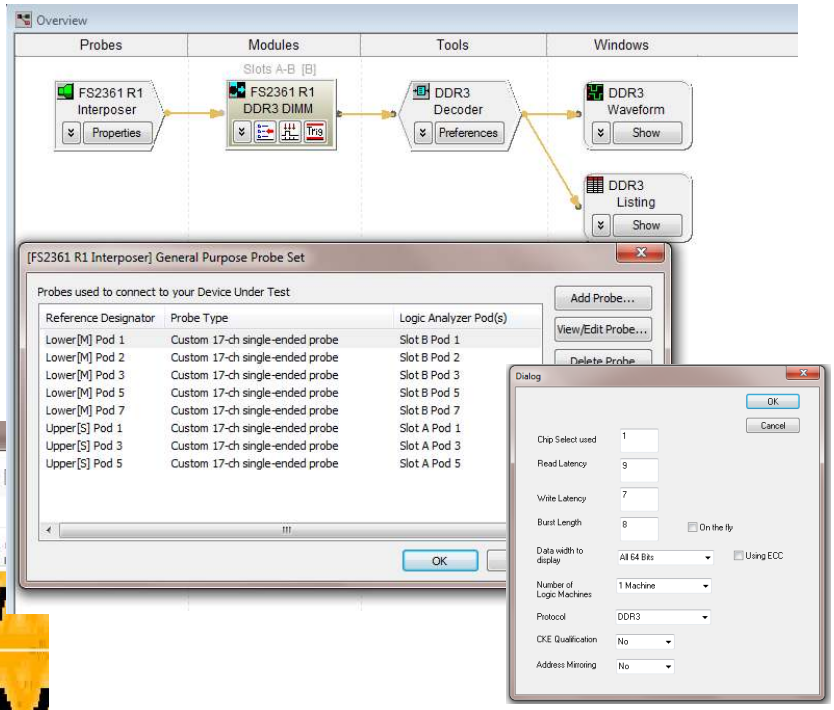


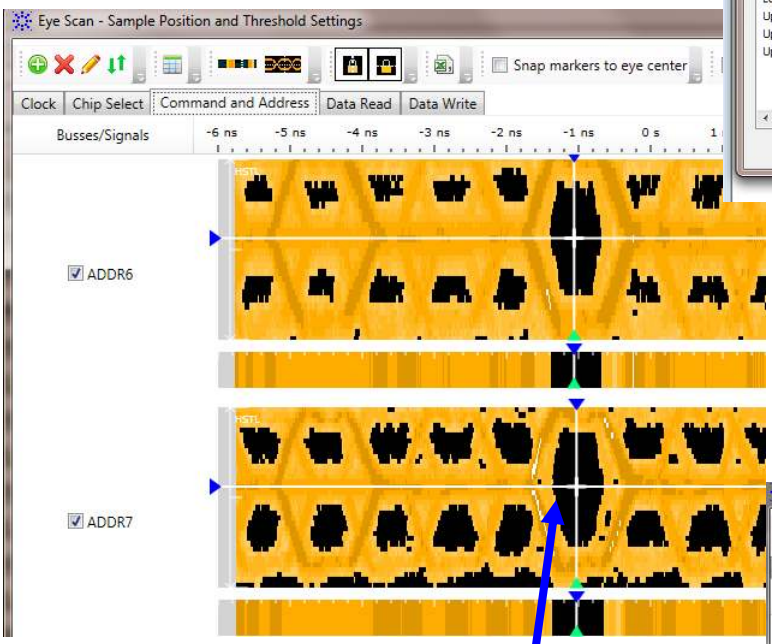
Quick and Accurate Setup

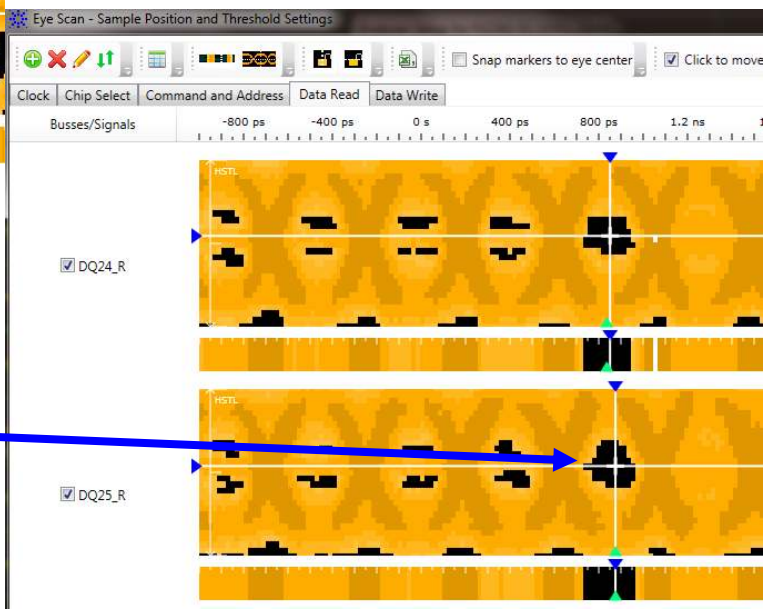
Get up and running quickly with Keysight EyeScan technology and the DDR Setup Assistant. A menu driven, step-by-step process guides you to setting bus timing parameters, voltage thresholds and individual sample positions easily.

As timing and voltage margins continue to shrink, confidence in signal integrity becomes an increasingly vital requirement of the design verification process. EyeScan lets you quickly acquire comprehensive signal integrity information on the DDR3 bus in your design, and can provide measurements with 5 ps of resolution.









Easy to set voltage thresholds and sample positions individually

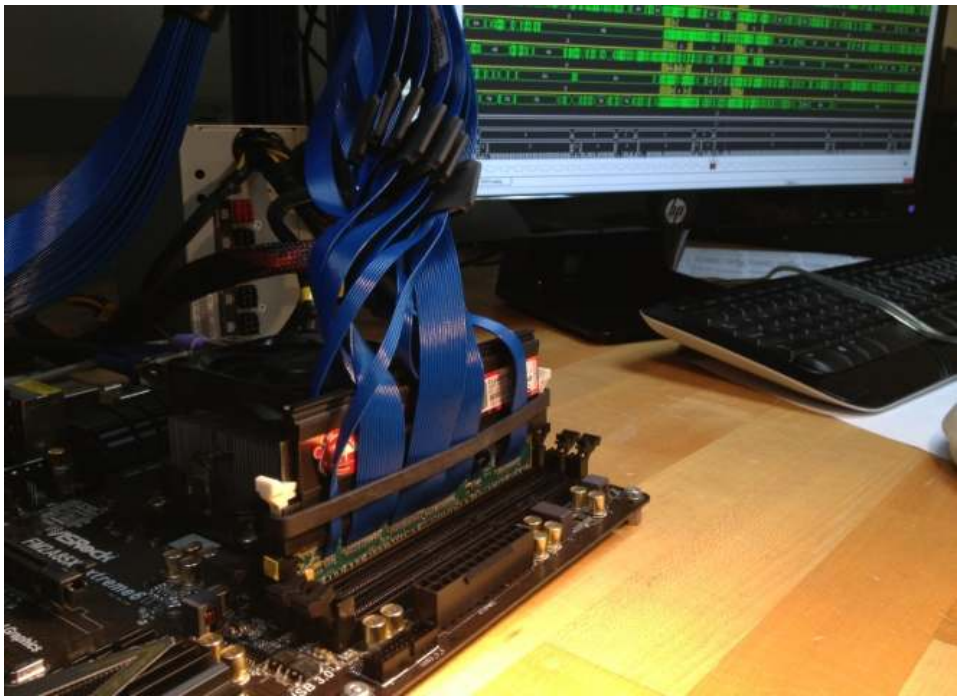
Ordering Information (contact FuturePlus for a quote)

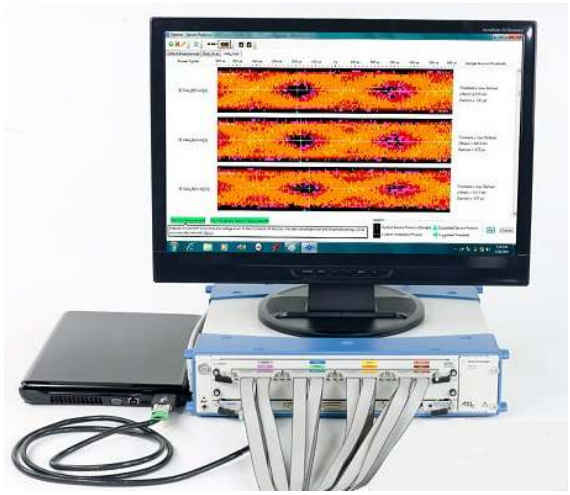
FS2361 – DDR3 2400 Interposer Probe of all A/C/C/DQ signals for use with Keysight U4154A Analyzers
FS236x – DDR3 2400 Interposer Probe of just A/C/C signals for use with Keysight U4154A Analyzers

Software included :
Configuration files and licensed FS1136 Protocol Decoder software for the Keysight logic analyzer

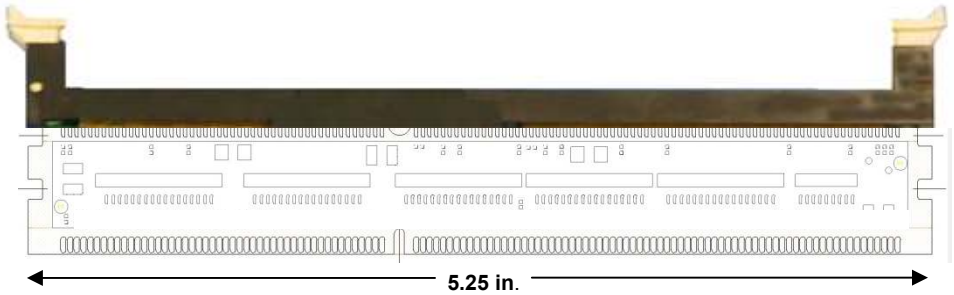
Keysight Logic Analyzer Requirements

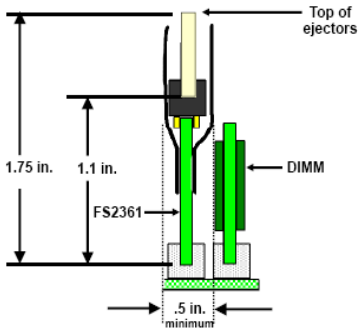
The FS2361 requires the U4154A. The requirements for 64 bit operation:
- Qty 1 M9502A two-slot AXIe chassis
- Qty 2 U4154A 136-channel Logic Analyzer Module (only 1 U4154A required for the FS236x)





FS2361 DDR3 DIMM interposer in a target system





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